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Unit

a plurality of pixel electrodes corresponding to respective pixels among the plurality of pixels;

a plurality of thin film transistors, each located outside of said plurality of pixel electrodes and comprising a plurality of conductive layers, for controlling supplying of signal voltage to the plurality of pixel electrodes;

a plurality of input terminals for receiving a control signal for the signal voltage to be supplied to the plurality of thin film transistors; and

wires, at least some of said wires being connected between said plurality of thin film transistors and said plurality of input terminals for sending the signal voltage from the plurality of input terminals to the plurality of thin film transistors, at least a portion of one of said wires having a lamination structure comprising two or more conductive layers formed of two or more layers used to form the thin film transistors, the portion of the wire being located near the peripheral area of the substrate and outside of said plurality of thin film transistors to function as an electric shielding wire.

3. (Twice Amended) A display apparatus having a plurality of pixels, comprising on a substrate:

a plurality of pixel electrodes corresponding to respective pixels among the plurality of pixels;

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a plurality of switching thin film transistors, each comprising a plurality of conductive layers, connected to the plurality of pixel electrodes, for supplying signal voltage to the plurality of pixel electrodes;

a plurality of driving thin film transistors, each comprising a plurality of conductive layers, arranged close to peripheral area of the plurality of pixel electrodes, for generating a driving signal for driving a number of switching thin film transistors;

a plurality of input terminals for receiving a control signal for driving the plurality of driving thin film transistors; and

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wires, at least some of said wires connecting the plurality of driving thin film transistors and the plurality of input terminals, at least a portion of one of said wires having a lamination structure comprising two or more conductive layers formed of two or more layers included in each switching thin film transistor and/or each driving thin film transistor, the portion of the wire being located near the peripheral area of the substrate and outside of said plurality of driving thin film transistors to function as an electric shielding wire.

Q1
15. (Amended) A display apparatus having a plurality of pixels, comprising on a substrate:
a plurality of pixel electrodes corresponding to respective pixels among the plurality of pixels;
Q2
a plurality of thin film transistors, each located outside of said plurality of pixel electrodes and comprising a plurality of conductive layers, for controlling supplying of signal voltage to the plurality of pixel electrodes;
Q3
a plurality of input terminals for receiving a control signal for the signal voltage to be supplied to the plurality of thin film transistors; and
wires, at least some of said wires being connected between said plurality of thin film transistors and said plurality of input terminals for sending the signal voltage from the plurality of input terminals to the plurality of thin film transistors, wherein each of the wires includes a first conductive layer formed of the lowest conductive layer of the thin film transistor and a second conductive layer situated above the first conductive layer and formed of other conductive layer of the thin film transistor, at least a portion of one of said wires being located near the peripheral area of the substrate and outside of said plurality of thin film transistors to function as an electric shielding wire.